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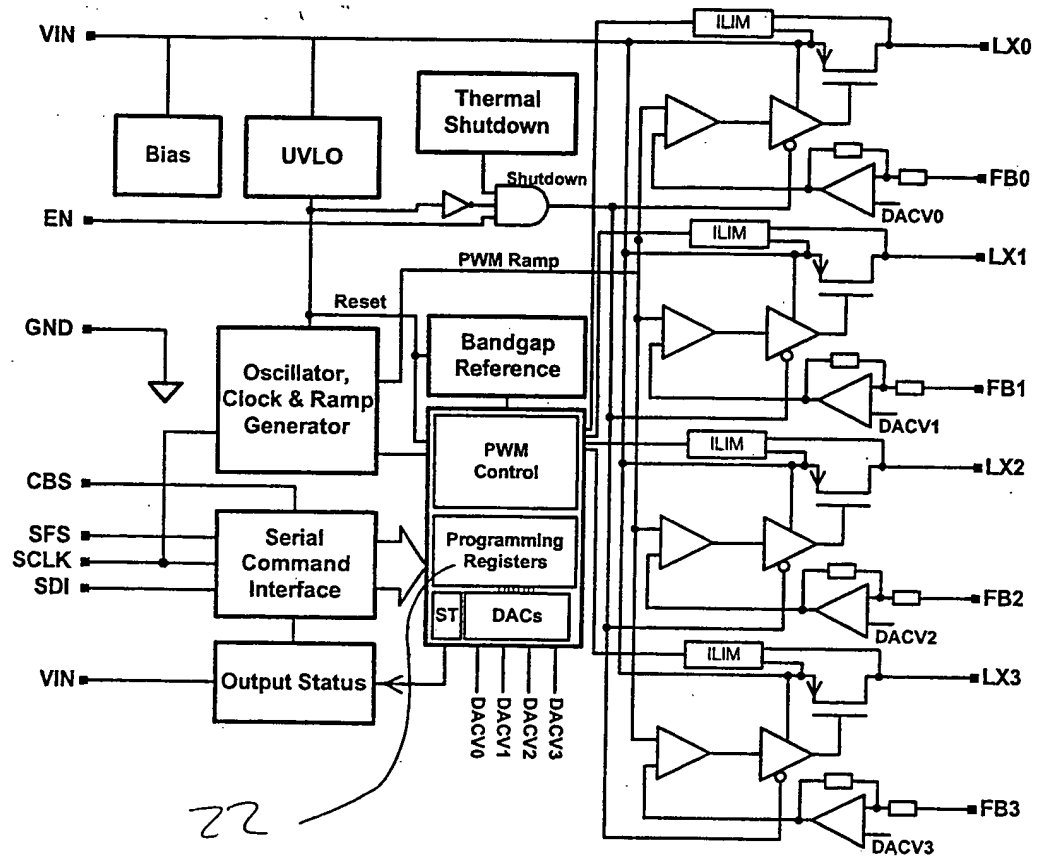


FIGURE 1

serial control interface timing diagram

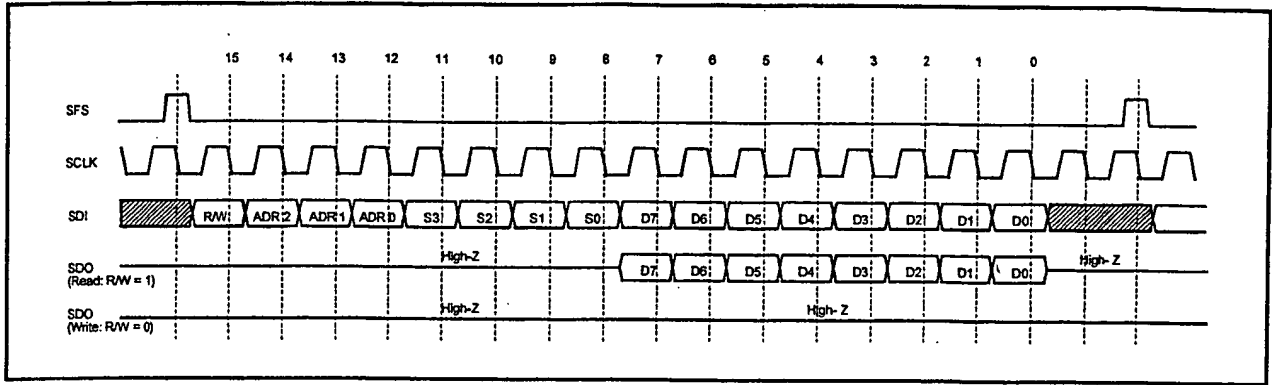


FIGURE 3

eight channel application circuit schematic

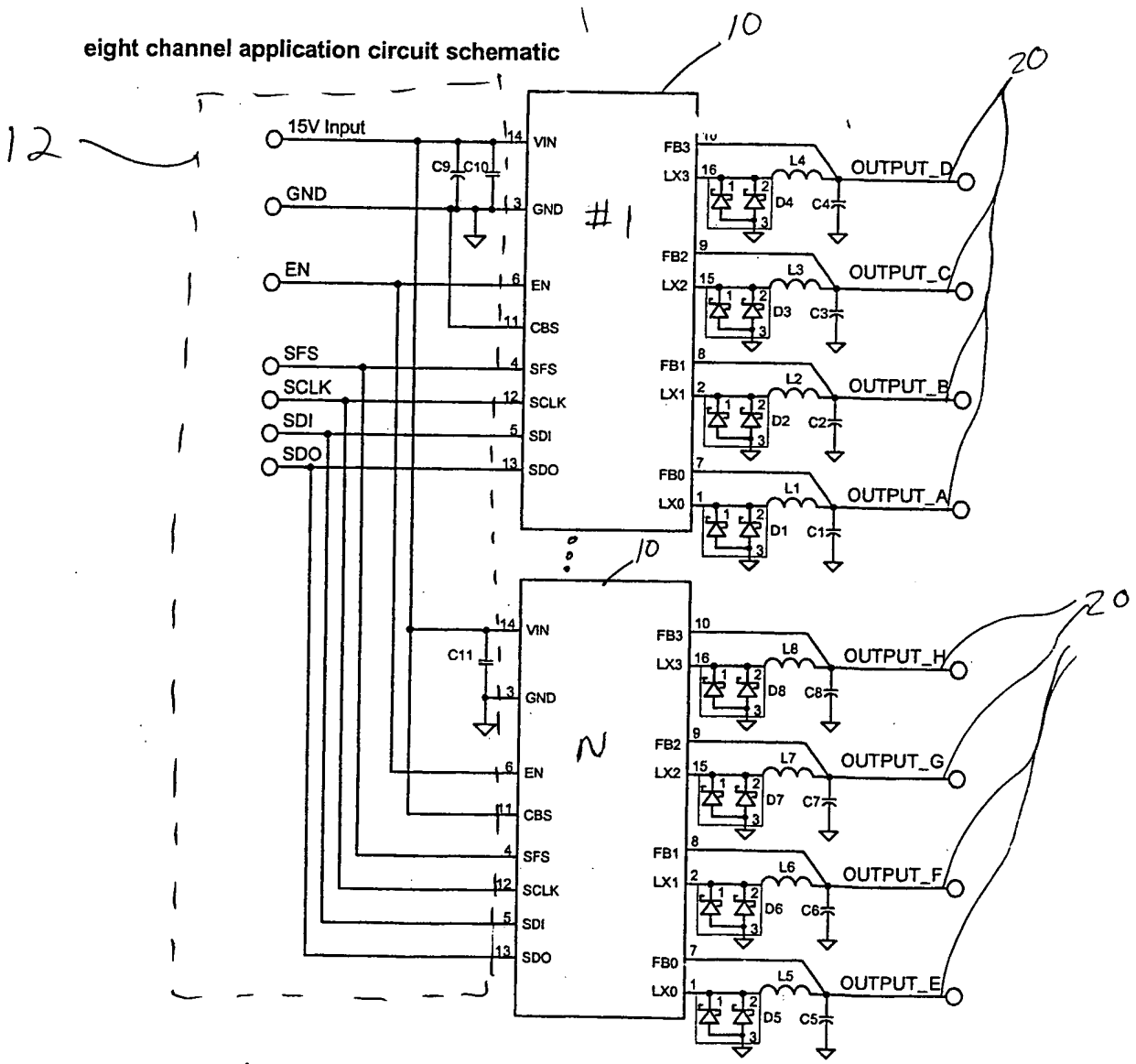


FIGURE 2

0934069-08101

SCLK

Initialization
word

Device 1
÷8 counter
state

Device 2
÷8 counter
state

Device 1
÷8 output

Device 2
÷8 output

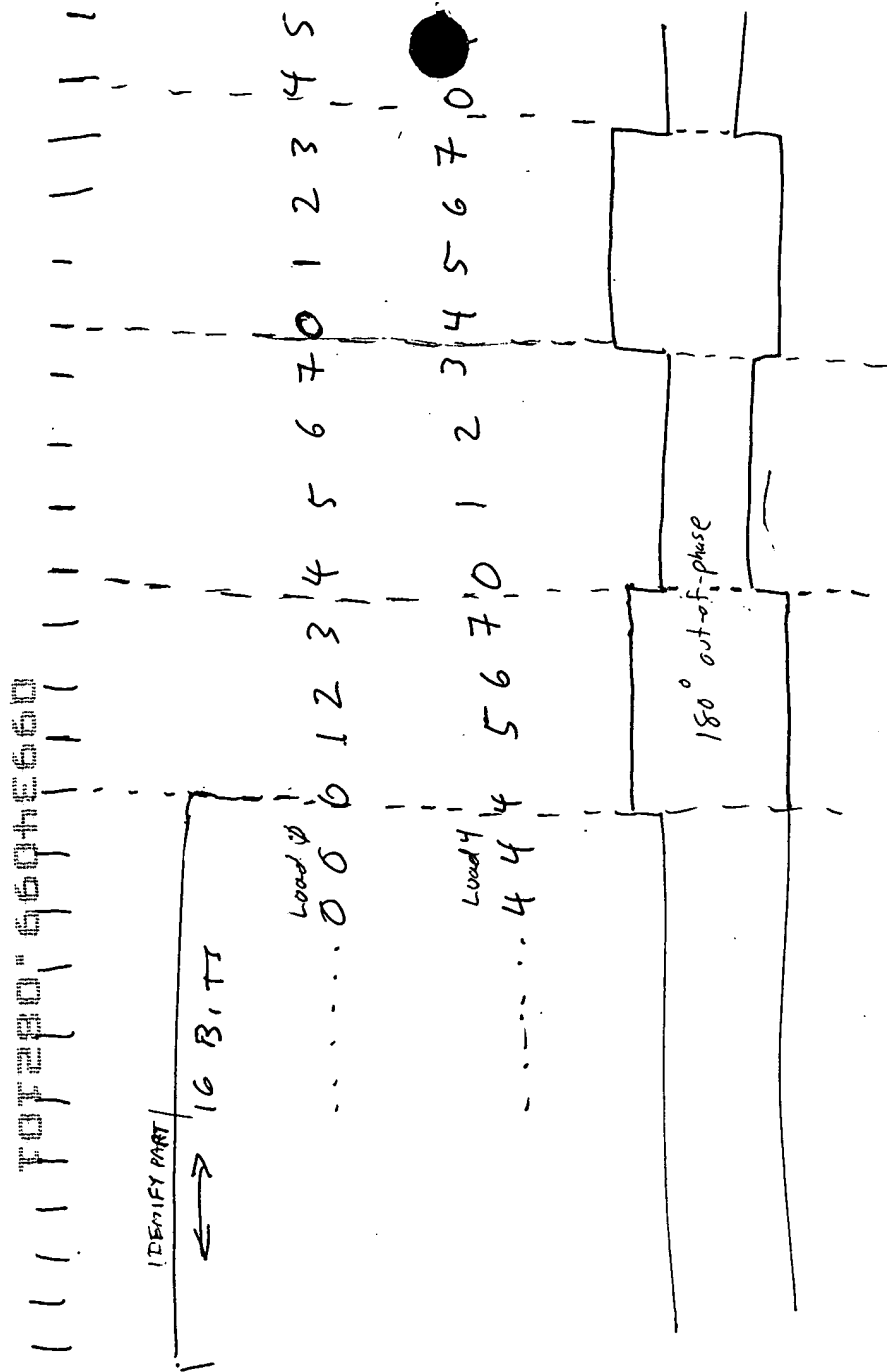
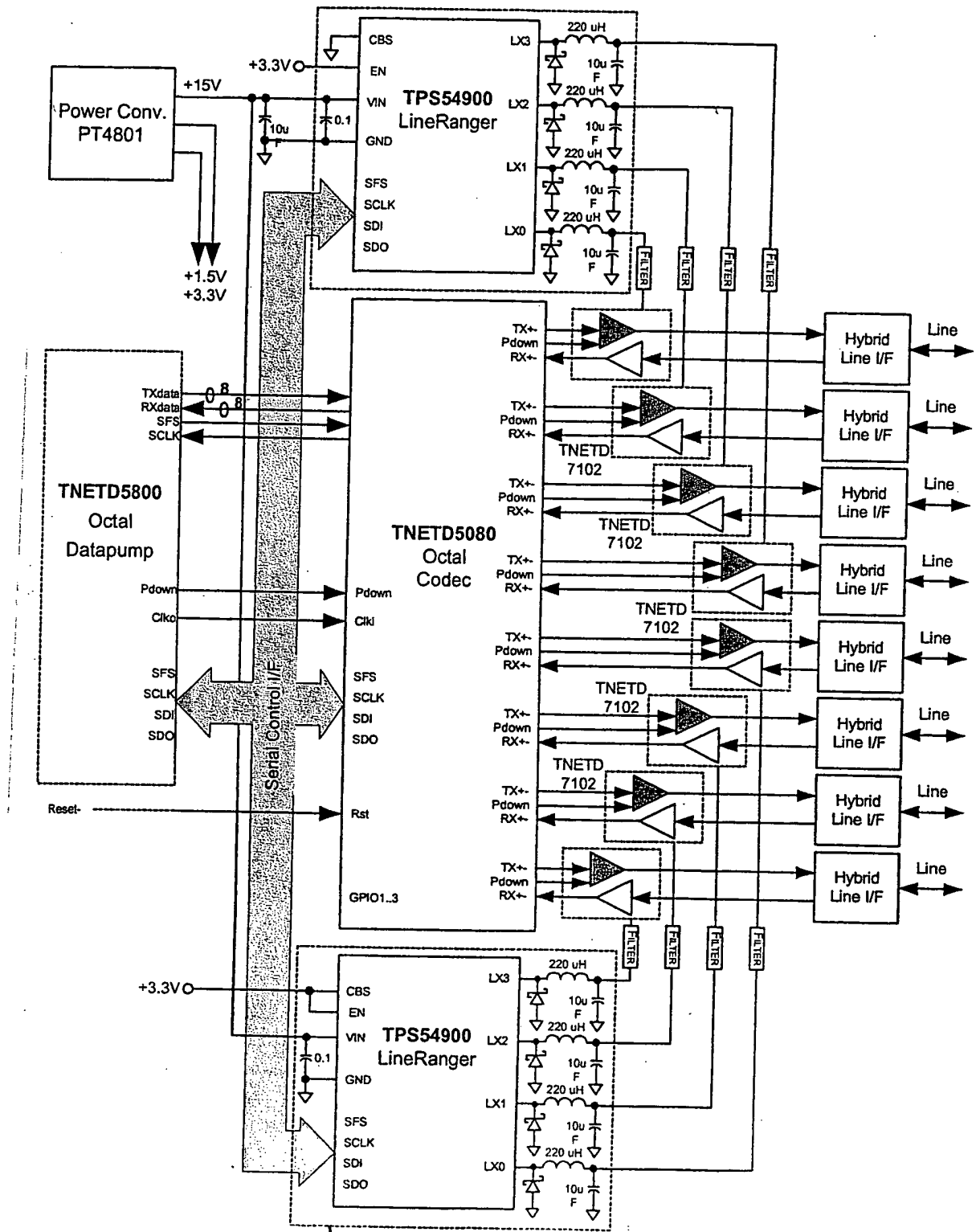


FIGURE 4

block diagram of eight channel AC5 line card with LineRanger option



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FIGURE 5